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J. F. Hayes  
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PATENT  
Attorney Docket No.: 6484.0073

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JC853 U.S. PTO  
09/740017  
12/20/00



In re Application of: :  
SHI-TRON LIN :  
Serial No.: Unknown : Group Art Unit: Unknown  
Filed: December 20, 2000 : Examiner: Unknown  
For: TRANSISTOR STRUCTURE FOR :  
ELECTROSTATIC DISCHARGE :  
PROTECTION CIRCUIT :  
:

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. § 1.97(b)**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicant brings to the attention of the Examiner the documents listed on the attached PTO 1449. This Information Disclosure Statement is being filed concurrently with the above-referenced application.

Copies of the listed documents are attached.

Applicant respectfully requests that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

LAW OFFICES

FINNEGAN, HENDERSON,  
FARABOW, GARRETT,  
& DUNNER, L.L.P.  
1300 I STREET, N.W.  
WASHINGTON, DC 20005  
202-408-4000

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and Applicant determines that the cited documents do not constitute "prior art" under U.S. law, Applicant reserves the right to present to the Patent Office the relevant facts and law regarding the appropriate status of such documents.

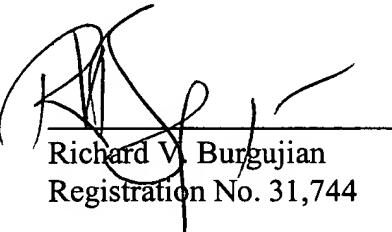
Applicant further reserves the right to take appropriate action to establish the patentability of the claimed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

By:

  
Richard V. Burgujian  
Registration No. 31,744

Date: 12/20/02

LAW OFFICES

FINNEGAN, HENDERSON,  
FARABOW, GARRETT,  
& DUNNER, L.L.P.  
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